

AMENDMENTS TO THE CLAIMS

(IN FORMAT COMPLIANT WITH THE REVISED 37 CFR 1.121)

1. (ORIGINAL) An apparatus comprising:

a first circuit configured to filter an analog input signal in an analog domain in response to one or more control signals;

5 a second circuit configured to convert said analog input signal to a digital signal; and

a third circuit configured to generate said control signals in response to said digital signal, wherein said third circuit is configured to control skewing of the analog input signal
10 within the first circuit to partially compensate for frequency dependent effects associated with a transmission medium.

2. (ORIGINAL) The apparatus according to claim 1, wherein said third circuit is further configured to calibrate said first circuit.

3. (ORIGINAL) The apparatus according to claim 1, wherein said third circuit is further configured to provide back-end digital processing control over said first circuit.

4. (ORIGINAL) The apparatus according to claim 3, wherein said first circuit is further configured to provide partial adaptation of said analog input signal in said analog domain.

5. (ORIGINAL) The apparatus according to claim 1, wherein said first circuit comprises:

a filter configured to tune said analog input signal; and
a processor configured to calibrate said filter.

6. (ORIGINAL) The apparatus according to claim 5, wherein said third circuit is further configured to offset said filter.

7. (ORIGINAL) The apparatus according to claim 1, wherein:

said first circuit comprises a analog tuned filter;
said second circuit comprises a analog-to-digital
5 conversion circuit; and
said third circuit comprises a digital signal processing
device configured to generate said control signals.

8. (ORIGINAL) The apparatus according to claim 1, wherein said first circuit comprises:

a current source;

a digitally switched capacitor array circuit;
a rectifier; and
a analog-to-digital converter, wherein said digitally switched capacitor array circuit, said rectifier and said analog-to-digital converter are configured to sweep over code values and determine a center value.

9. (ORIGINAL) An apparatus comprising:

means for tuning an analog input signal in an analog domain in response to one or more control signals;

means for converting said analog input signal to a digital signal; and

means for generating said control signals in response to said digital signal, wherein said third circuit is configured to control skewing of the analog input signal within the first circuit to partially compensate for frequency dependent effects associated with a transmission medium.

10. (ORIGINAL) A method for controlling skewing of an input signal to partially compensate for frequency dependent effects associated with a transmission medium comprising the steps of:

(A) tuning said input signal in an analog domain in response to one or more control signals;

(B) converting said input signal to a digital signal;
and

10 (C) generating said control signals in response to said
digital signal.

11. (ORIGINAL) The method according to claim 10, wherein
step (A) further comprises:

providing partial adaption of said input signal in the
analog domain.

12. (ORIGINAL) The method according to claim 10, wherein
step (A) further comprises:

filtering said input signal with a filtering device; and
calibrating said filtering device.

13. (ORIGINAL) The method according to claim 12, wherein
step (A) further comprises:

controlling an offset of said filtering device.